

## Advin Systems Inc.

556 E. Weddell Drive, #8, Sunnyvale, CA 94089, USA.

Phone: 408-243-7000 Fax: 408-541-9006

www.Advin.com Sales@Advin.com



- Proven Industrial Quality
- Free Lifetime S/W Updates via WEB
- True Low Voltage Support down to 1.8v
- Gang/Set Expandable
- CE Certified

Shown withOptional PLCC module (UA-44)

# PILOT-U44 *Plus*Advanced 44-Pin Universal Device Programmer

#### **UNIVERSAL AND FLEXIBLE:**

- One unit supports all types of programmable devices: PALs, GALs, parallel and serial PROMs, E/EPROMs, FLASH memories, microcontrollers, Altera MAX devices, AMD MACHs, Lattice isp/pLSI, WSI PSDs, Xilinx EPLDs, and many others.
- Provides true low voltage support for new devices which require low levels of Vcc and digital inputs. (i.e. supports devices which are not 5-volt tolerant.)
- Software controlled from desktops or notebooks based on 386, 486, Pentium, or compatibles. Easy new device updates via software from floppies, BBS or Internet (WEB).
- Pins are controlled by programmable software pin drivers.
- Device technologies supported include CMOS, BiCMOS, NMOS, HMOS, EE erasable, Flash, bipolar, ECL, etc.
- PLCC, LCC, TSOP, PSOP, SOIC, PGA, BGA, QFP and TQFP devices optionally supported by reliable Advin-made modules, not from third party vendors.
- Expandable to support high pin-count devices up to 128 pins.

#### COMPLETE MEMORY/MICRO DEVICES SUPPORT:

- Supports EPROMs from 2716 up to the newest 64 megabit (and beyond) EPROMs and FLASH memories.
- Accepts various file formats including Intel HEX, Intel Extended Hex, Motorola S-records, POF, ASCII and binary.
- Virtual memory feature: makes use of RAM and disk space on your PC. No RAM expansion modules ever needed, even for programming large size devices.
- Automatic splits (1 to 2, 1 to 4, 1 to 8) for both 8-bit and 16-bit memories.

- Supports all programming algorithms including Standard, Fast, Intelligent, Quick-Pulse, Flashrite, etc,-- and exactly according to IC manufacturers' specifications.
- Supports advanced device functions such as individual sectorprotection and un-protection, S/W write protection, programming of configuration words, etc.
- Functions provided include: read, program, verify, sector protect, edit, checksum, file offset, buffer offset, partial address programming, ASCII buffer edit, etc.
- Release control features: automatically generates serial numbers, checksums, and date/time stamping information for memory devices.

#### COMPLETE LOGIC DEVICE SUPPORT:

- Accepts POF files from ALTERA, standard JEDEC outputs from CUPL, MINC, ABEL, PALASM, ORCAD, etc.
- Test vector function (functional testing) automatically reports failed pins and states.
- Convenient screen-based editing of fuse values and test vectors.
- Other functions include: read, program, erase, verify, security, checksum, automatic PAL to GAL JEDEC files conversion, etc.

#### **SOFTWARE USER-FRIENDLY:**

- Powerful PC-based software gives you more power, utilities and conveniences than stand-alone programmers.
- Full screen human interface provides plenty of useful information.
- Batch/macro facility allows you to put frequently-used commands into command files. It also means allowing non-technical personnel to easily perform repeated command sequences.
- Free lifetime software updates: simply download from BBS or WEB.

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#### HARDWARE USER-FRIENDLY:

- Interfaces to PC through standard parallel printer port. No need to open up your PC, remove and reinstall special interface cards every time you move the programmer from one PC to another.
- Parallel interface eliminates the slowness and clumsiness of serial communication during normal operation.
- New device support via software, not firmware.
- Reverse-device insertion check warns operator of accidental reverse placement of devices.
- Continuity check warns operator of misplaced device or broken device pins.
- Universal power supply automatically accepts input voltages from 85v AC to 264v AC. No need to switch between 110/230.

#### **DEPENDABLE AND RELIABLE:**

- All programming signals are generated from programming instrument, not from a card inside the PC. Signals are noise-free and accurate.
- Metal chassis shields programmer from potentially damaging external static charges.
- Built-in power supply provides adequate and isolated power for programmer, avoids power deficiency problems common in smaller programmers.
- All sockets used have gold-plated contacts and are of the best quality in the industry.
- Designed and manufactured by a company that has over ten years of experience in making and supporting programming instruments.
- Approved by IC manufacturers.

• Made in Silicon Valley, California, USA, in proximity to many of the world's leading semiconductor companies.

#### **GUARANTEE, WARRANTY AND SUPPORT:**

- 30-day unconditional money-back satisfaction guarantee.
- 1 year limited hardware warranty, including parts and labor.
- Factory-direct technical support.
- Free lifetime software updates via 24-hour BBS or WEB.

#### **GANG/SET EXPANDABLE:**

Available gang modules from Advin expand the machine into a Gang/Set programmer capable of programming eight EPROMs, EEPROMs, FLASH, or micros. Package types include DIP, PLCC, TSOP, PSOP, QFP, TQFP, ICSP In-circuit Programming, etc. For a complete list of gang modules, please visit our website at: http://advin.com/gang-programmer-modules.htm



#### **SPECIFICATIONS**

#### Pin Drivers

44 pin drivers. Each pin is software programmable to generate either digital or analog voltages.

Minimum slew rate: .001V/us; Maximum slew rate: 1000V/us. Range: 0 to 25.5V in 100mV increments. Current limited.

#### Device sizes supported

On standard equipment: up to 44 pins DIP. With UPA-44: up to 44 pins PLCC. With optional modules: up to 128 pins. Functional testing: up to 44 pins.

For complete details, please see accompanying Supported Devices List.

#### Low Voltage Capability:

All Vcc levels are supported, including 6.5v, 5v and as low as 1.8v.

#### Hardware Upgradability:

Upgradable to PILOT-U84-Plus, PILOT-U128-Plus.

#### **Hardware Expandability:**

Supports many different device packages with optional add-on modules: PLCC, LCC, TSOP, PSOP, SSOP, SOIC, PGA, QFP, TQFP, etc. Expandable to support Gang/Set programming of EPROMs and Flash using Gang Modules.

#### **Special Functions**

Reverse device insertion check. Continuity check. Automatic diagnostics and calibration checks.

#### **Examples of available operations:**

Release-control Address

Macro Operator Prompt

Configure Device Configure Width Config Set-size Config Algo Configure Security Configure Port Configure Save Configure Others Device Program Device Erase Device Secure Device Chksum Device Blank-Check Device Verify Device Test Device Examine File Name File Load File Directory File Format Buffer Edit Fuse Buffer Checksum File Save File Address Buffer Edit Vector Buffer Edit UES Buffer Fill Buffer Load Buffer Invert Buffer Initialize Even/odd byte swap Macro JMP,JBE,JE Macro Execute Macro Label Active Range Sector Protect n Sector Protect All Sector Un-protect Release-control Serial # Enter Release-control Serial # Increment

#### Macro/Batch Facility

Similar to DOS batch files. Chaining of Macros: Yes. Nesting: No. Number of parameters allowed: 9.

#### File Formats Supported

Logic devices: Industry standard JEDEC, Altera POF. Memory devices, micro controllers: ASCII, Binary, Intel HEX. Extended Hex. Motorola S-records.

#### Programming through-put, examples (in min:sec)

GAL 16V8 0:03	MACH 110	0:05
TIBPAL22V100:02	Atmel 29C010	0:16
Xilinx 1765D 0:04	27C010A	0:25
	106	

Note: Assuming programmer is controlled by 486 computer. Programming pulse length is independent of computer speed. Programming overhead varies with computer speed.

#### **System Requirements**

IBM 386/486/Pentium/Notebook or compatible machines. No PC-slot required. DOS 3.1 or above, 640K RAM. Hard disk with at least 1.5 MB disk space. One parallel printer port (LPT1, 2 or 3).

#### **System Interface**

PC connection: ..... standard parallel printer port Equivalent transfer rate: ... at least 200K baud

#### Electrical

85v to 264v, automatic switch Operating voltage: .....

Power consumption: ......

Power connector: ..... Standard IEC

#### Physical

One 44-pin gold ZIF, accepts .3-.6" DIPs Socket ..... Number of 50-pin headers used for add-on module connections ......1

Instrument weight: ..... 

Supplied Equipment: Programming hardware, control software, user manual, interface cable to PC parallel printer port, detachable power cord. (power cord included for domestic customers only).

#### **Optional Equipment**

BGA, PLCC, SOIC, TSOP, PSOP, SSOP, PGA, QFP, TQFP, QFN, LAP modules; Gang/Set modules; Im-Circuit Programming Modules.

Release-control Date/Time Stamp